

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
12 May 2005 (12.05.2005)

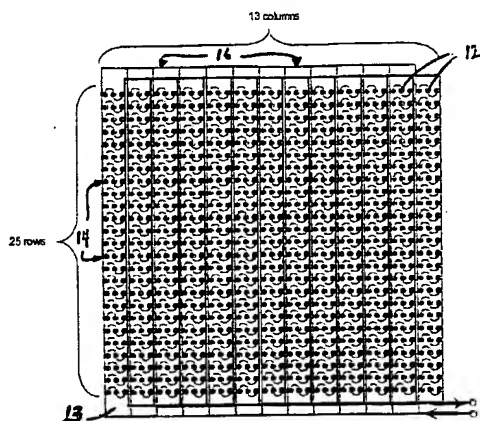
PCT

(10) International Publication Number  
**WO 2005/043954 A2**

- (51) International Patent Classification<sup>7</sup>: **H05B**
- (21) International Application Number:  
PCT/US2004/036046
- (22) International Filing Date: 29 October 2004 (29.10.2004)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:  
60/516,381 31 October 2003 (31.10.2003) US
- (71) Applicant: **PHOSEON TECHNOLOGY, INC.**  
[US/US]; 14974 N.W. Greenbrier Parkway, Beaver-  
ton, OR 97006 (US).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **BEDSON, Jon, R.**  
[US/US]; 12425 nw BARNES ROAD #49, Portland, OR  
97229 (US). **MCNEIL, Thomas, R.** [US/US]; 5410 N.W.  
Edgebrook PLace, Portland, OR 97229 (US). **OWEN,  
Mark, D.** [US/US]; 16075 N.W. Telshire Drive, Beaverton,  
OR 97006 (US).
- (74) Agent: **WOLFE, James, L.**; Ganz Law, P.C., P.O. Box  
2200, Hillsboro, OR 97123 (US).
- (81) Designated States (unless otherwise indicated, for every  
kind of national protection available): AE, AG, AL, AM,  
AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,  
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,  
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,  
KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD,  
MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG,  
PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM,  
TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM,  
ZW.
- (84) Designated States (unless otherwise indicated, for every  
kind of regional protection available): ARIPO (BW, GH,  
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,  
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),  
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,  
FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI,  
SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,  
GW, ML, MR, NE, SN, TD, TG).
- Published:  
— without international search report and to be republished  
upon receipt of that report

[Continued on next page]

(54) Title: SERIES WIRING OF HIGHLY RELIABLE LIGHT SOURCES



(57) Abstract: The light array of this invention includes a number of columns and rows of LED's connected in a series/parallel combination. The series parallel combinations effectively optimize the impedance, accommodate failure rate, facilitate light mixing, provide a means of imbedding redundancy, and common cathodes or anodes. This arrangement provides a superior light source for consumer, industrial and specialty markets in respect to mean time between failure, process control, radiant intensity, wavelength mixing, power requirements and other characteristics of the light source. Each column includes a number of rows of plural LED's. The LED's in each row are wired in series and each column is wired in parallel so that if one LED fails only the LED's connected in series with the failed LED will also fail. There is redundancy in the circuit as well as the arrays so that if there are failures different current carrying elements or different series LEDS will automatically be powered on. The array may be connected in series with one or more LED arrays to form a module. Multiple modules may be connected in series with other multiple modules.

WO 2005/043954 A2

WO 2005/043954 A2



---

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*